

**PATENT**

**Expedited Procedure  
Under 37 C.F.R. § 1.116  
Technology Center 2800**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:** James M. Derderian

**Serial No.:** 09/938,106

**Filed:** August 23, 2001

**For:** METHODS FOR ASSEMBLING  
SEMICONDUCTOR DEVICES WITH  
DISCRETE CONDUCTIVE ELEMENTS  
SEPARATING ADJACENT,  
SUPERIMPOSED SEMICONDUCTOR  
DEVICES

**Confirmation No.:** 1038

**Examiner:** J. Im

**Group Art Unit:** 2811

**Attorney Docket No.:** 2269-4832US  
(01-0104.00/US)

**VIA ELECTRONIC FILING  
December 21, 2007**

**RESPONSE TO FINAL OFFICE ACTION**

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Examiner:

This Amendment follows the final Office Action of October 22, 2007, the shortened statutory period for response to which expires on January 22, 2008. This response is being submitted within two months of the mailing date of the final Office Action.

A listing of claims starts at page 2 of this paper; and

Remarks begin on page 8 of this paper.